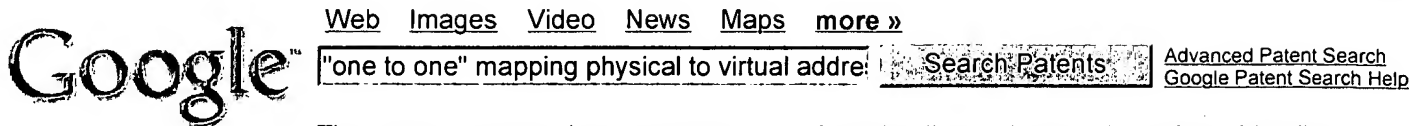


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Patent Search Patents 1 - 10 of 15 on "one to one" mapping physical to virtual address and DMA. (0.00 second)

Method and system for initiating and loading DMA controller registers by using user-level programs

US Pat. 5659798 - Filed Feb 2, 1996

Mapping of **virtual** memory uses as a source or destination for UDMA data ...
as the source or destination of a **DMA** transfer by the desired **physical** address ...

Software emulation of bank-switched memory using a virtual DOS monitor and paged memory management

US Pat. 4926322 - Filed Aug 3, 1987 - Compaq Computer Corporation

I/O Address for **Mapping** Registers Window 15 20 25 30 35 40 The **DMA** ... only linear
address range which is not mapped linear-equal-to-**physical** (**one-to-one**) ...

Migrating a memory page by modifying a page migration state of a state machine associated with a DMA mapper based on a state notification from an operating system kernel

US Pat. 6804729 - Filed Sep 30, 2002 - International Business Machines Corporation

This **mapping** is recorded in the **I/O virtual address** to **physical address** table (step 309). The **DMA I/O virtual address mapping** ...

Method, system, and computer program product for invalidating pretranslations for dynamic memory removal

US Pat. 6918023 - Filed Sep 30, 2002 - International Business Machines Corporation

In other platforms, a **DMA mapping** goes through an **I/O** page table, which converts
an **I/O virtual address** to a **physical** memory **address**. ...

Apparatus and method for object-oriented memory system

US Pat. 6594708 - Filed Mar 26, 1998 - Sun Microsystems, Inc.

The particular **virtual address** space is to be the one which the thread calling
... range of **physical** memory, then do the **mapping** for that smaller sub-range. ...

Method for communicating interrupt data structure in a multi-processor computer system

US Pat. 6021456 - Filed Nov 12, 1996

The first way is a direct mapped approach, where the **physical** map-ping is performed
on a **one to one** basis with an **address** offset. The second **mapping** is ...

Memory management unit with programmable EDAC descriptors

US Pat. 6609181 - Filed Apr 9, 2001 - Goodrich Corporation

This **one-to-one** **mapping** of **virtual address** to **physical address** for each ...
the MMU 14 will accept **virtual** addresses from the CPU, **DMA** controller and other ...

Texture download DMA controller synching multiple independently-running rasterizers

US Pat. 6677952 - Filed Jun 9, 2000 - 3Dlabs Inc., Ltd.

(This assumes a **one to one** **mapping** between texels and pixels and takes ...
This is a **virtual** Page host page or a **physical** host page as indicated by the ...

System and method for detecting access to shared structures and for maintaining coherence of derived structures in virtualized multiprocessor systems

US Pat. 6961806 - Filed Dec 10, 2001 - VMware, Inc.

... is assumed here that there is a **one-to-one mapping** from **virtual** to **physical** cpus.

... trace activity is synchronous (with the exception of device **DMA**). ...

Asynchronous transfer mode (ATM) segmentation and reassembly unit **virtual address** translation unit architecture

US Pat. 5983332 - Filed Jul 1, 1996 - Sun Microsystems, Inc.

The **address** should not be in the ATU because it is a new **virtual-physical mapping**.

If the **address** is present then that means that the **address** was not ...

"one to one" mapping physical to virtual address

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identity-mapped address

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Patent Search

Patents 1 - 3 of 5 on identity-mapped address. (0.00 seconds)

Real mode translation look-aside buffer and method of operation

US Pat. 6301647 - Filed Dec 17, 1997 - VIA-Cyrix, Inc.

25 The physical **address** A(31:12) output from multiplexer 1302 is also used ...
operation and **identity- mapped** physical addresses associated with a real mode ...

Quick starting for microprocessor-based system by retrieving a target state memory image and a target state data structure from an image storage medium

US Pat. 6636963 - Filed Dec 30, 1999 - Cardiac Pacemakers, Inc.

This way, for any calculations that will require **identity mapped address** (same
with and without paging, so that the linear **address** equals the physical ...

Translation look-aside buffer for storing region configuration bits and method of operation

US Pat. 6351797 - Filed Nov 13, 1998 - VIA-Cyrix, Inc.

... operation and **identity- mapped** physical addresses associated with a real mode
... The reference **address** A(31:12) is shown split into reference **address** ...

identity-mapped address

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DMA and "virtual pointer"

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Patent Search

Patents 1 - 5 of 7 on DMA and "virtual pointer". (0.00 seconds)

User scheduled direct memory access using virtual addresses

US Pat. 5301287 - Filed Feb 16, 1993 - Hewlett-Packard Company

A device as in claim 19, wherein said transferring means comprises **DMA** ...
as a command/pointer packet including a **virtual pointer** to said associated data ...

Method and system for simulating auto-init mode DMA data transfers

US Pat. 5875289 - Filed Jun 28, 1996 - Microsoft Corporation

A virtual read pointer Rv is associated with the **DMA** buffer. ... device driver
to obtain the position of **virtual pointer** Rv. The application then inserts ...

Programmably configurable host adapter integrated circuit including a RISC processor

US Pat. 5659690 - Filed Oct 15, 1992 - Adaptec, Inc.

Pointer to host adapter structure field (Table 42) is a **virtual pointer** to the host
... **DMA** channel number field (Table 42), which is used for an ISA host ...

Network adapter for inserting pad bytes into packet link headers based on destination service access point fields for efficient memory transfer

US Pat. 5491802 - Filed May 29, 1992 - Hewlett-Packard Company

A data path 53 represents a direct memory access (**DMA**) transfer operation from
... This process is accomplished by simple **virtual pointer** manipulation and ...

Insertion of network data checksums by a network adapter

US Pat. 5430842 - Filed May 29, 1992 - Hewlett-Packard Company

A processor would have to copy the bytes to an sends a **DMA** transaction from ...
Therefore, three pie **virtual pointer** manipulation and eliminates the need ...

DMA and "virtual pointer"

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